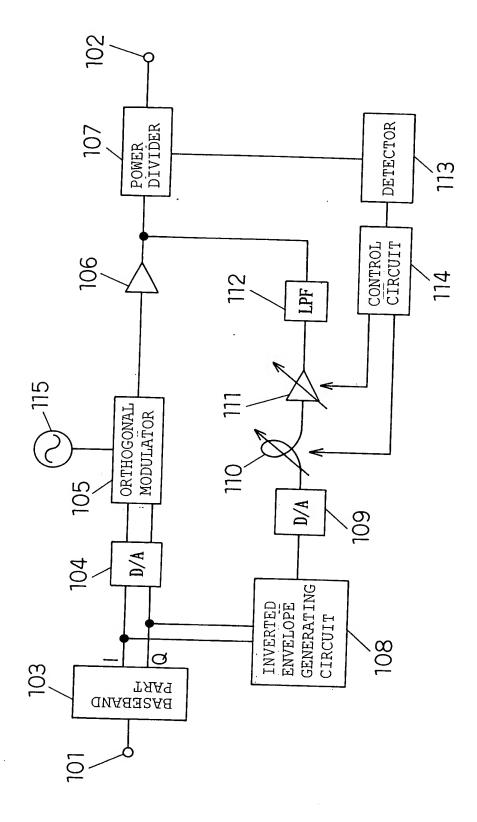
Fig. 1



F 1 00

Fig. 3 (a)

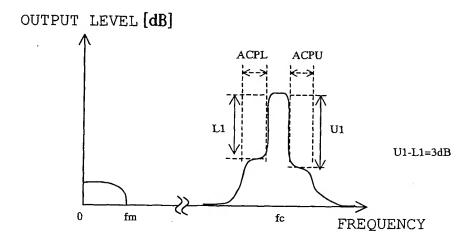
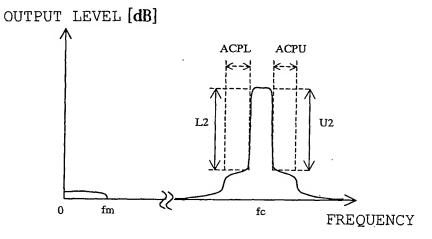


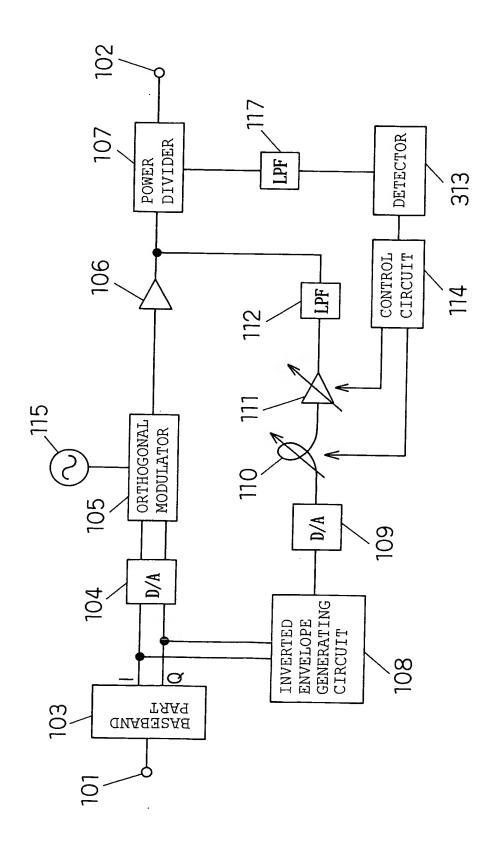
Fig. 3 (b)



U2-U1=8dB

203 204

Fig. 6



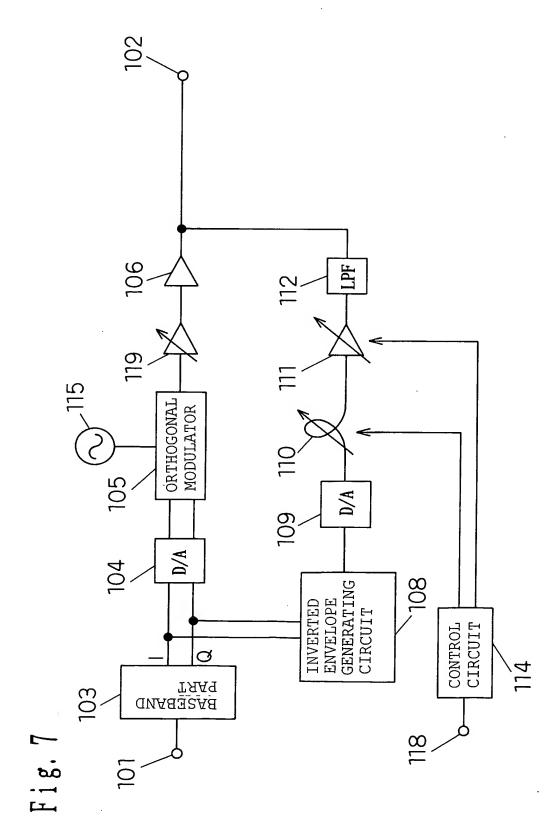
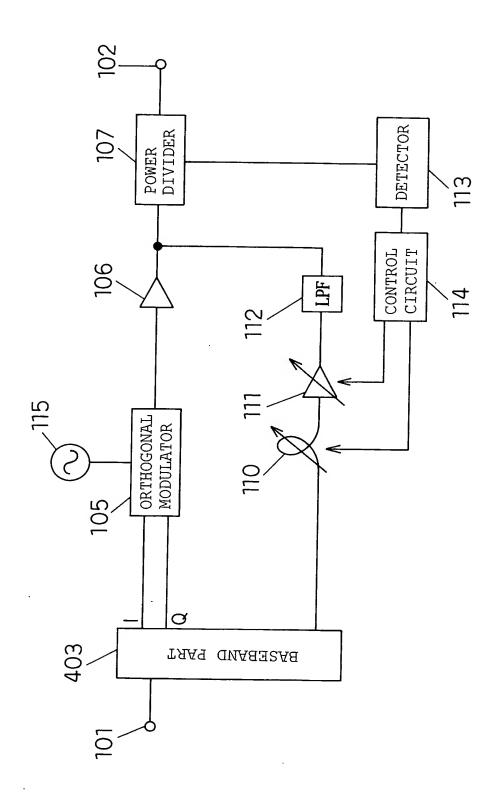
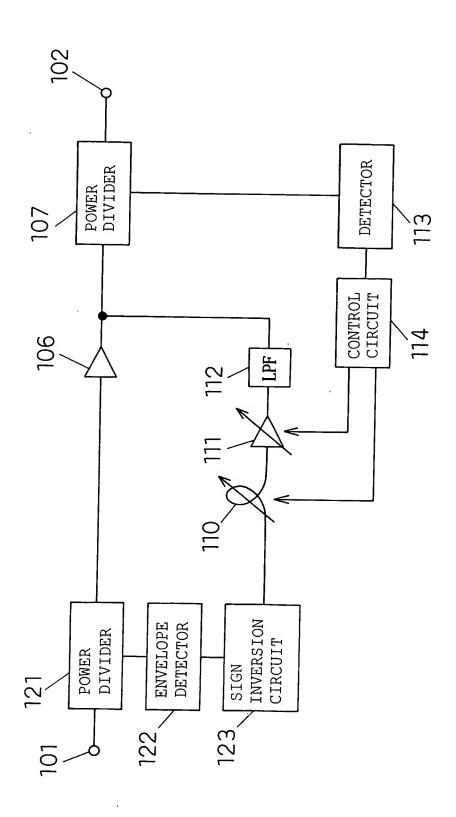


Fig. 8





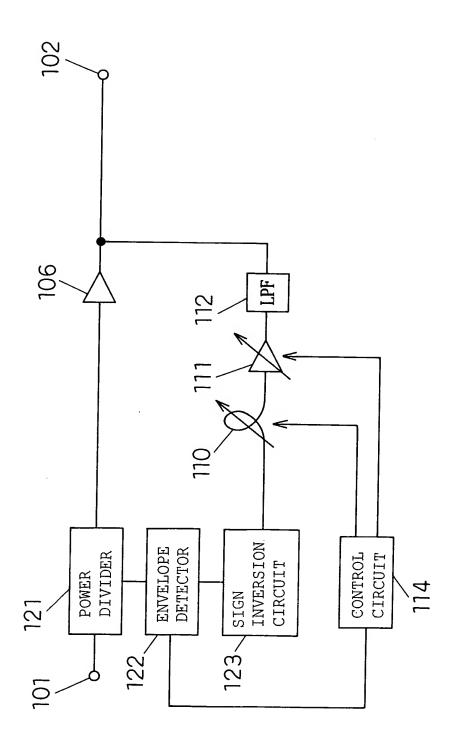
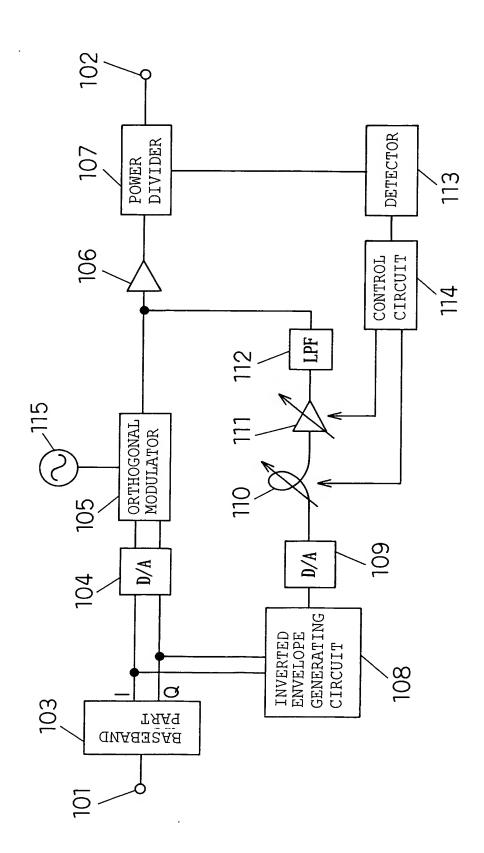


Fig. 11



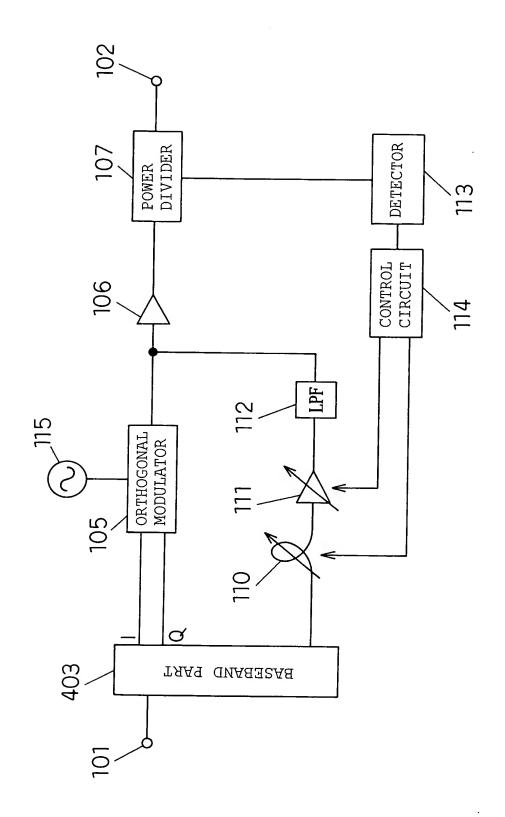
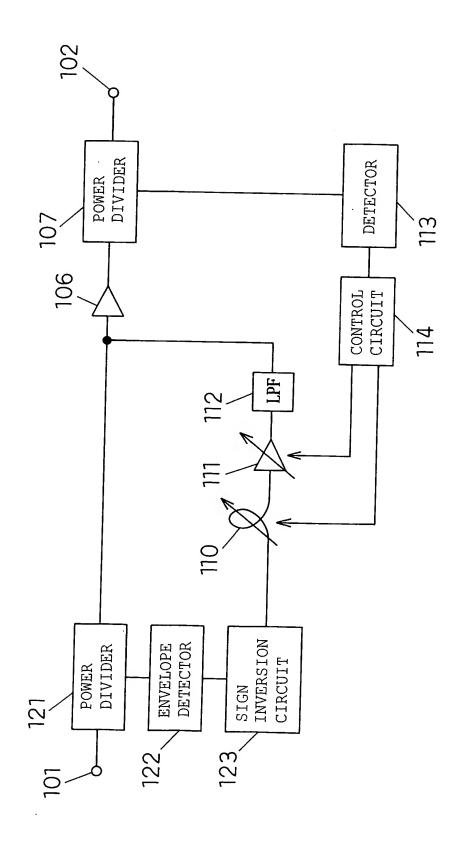


Fig. 13



DETECTOR POWER DIVIDER 132 POWER DIVIDER CONTROL CIRCUIT 106 134 GENERATING DISTORTION 131 CIRCUIT LPF ORTHOGONAL MODULATOR D/A GENERATING INVERTED ENVELOPE CIRCUIT 108 Ø BASEBAND PART Fis. 1

102

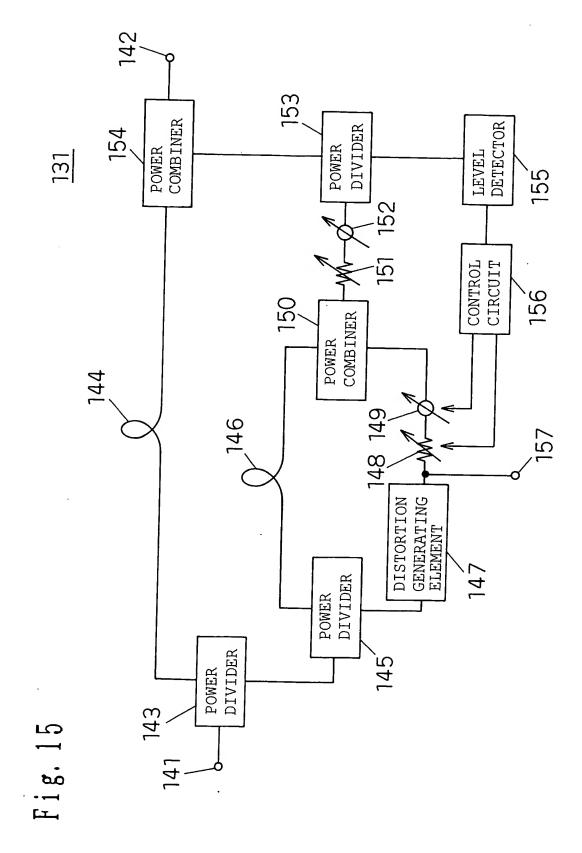
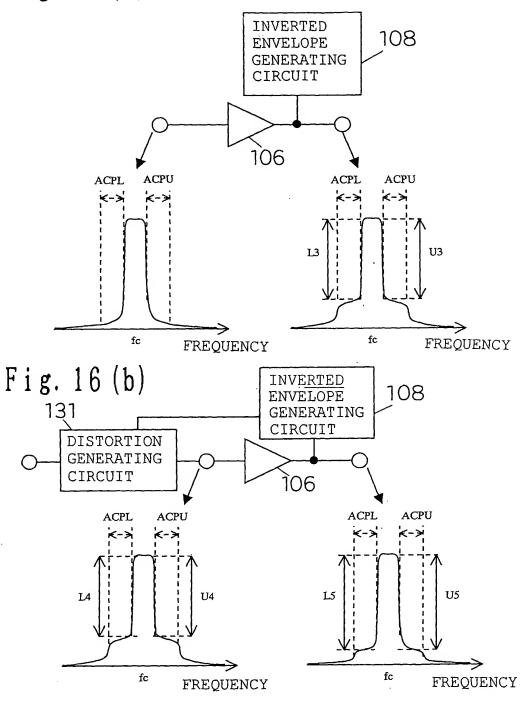
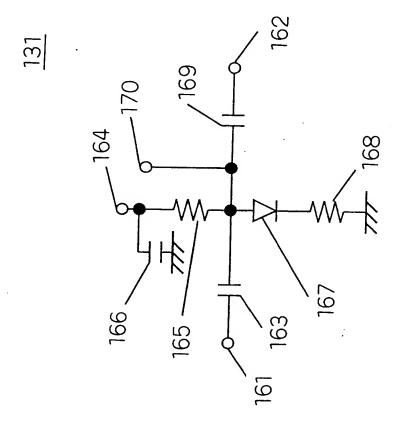


Fig. 16 (a)



U5-U3=10dB



-- 1 -- 6- 0

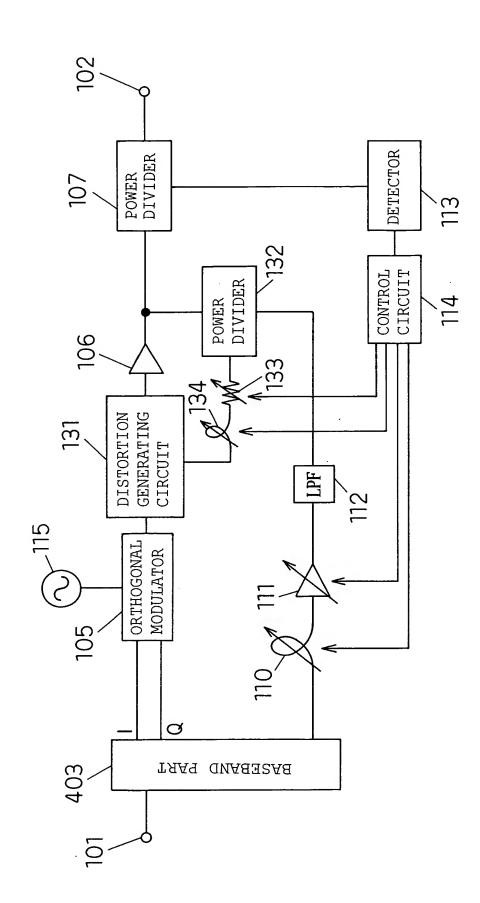
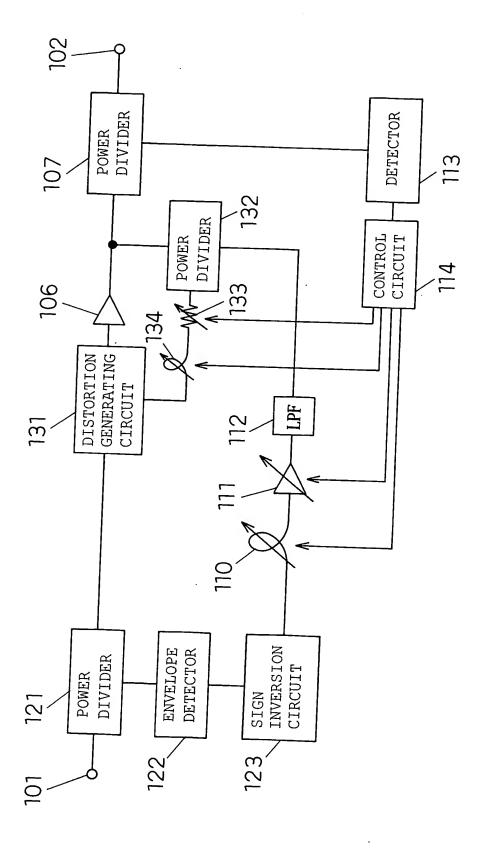
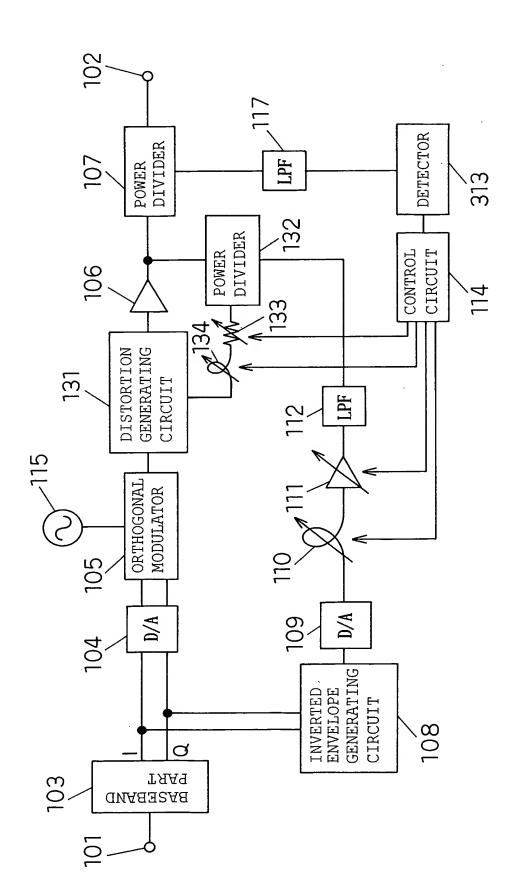
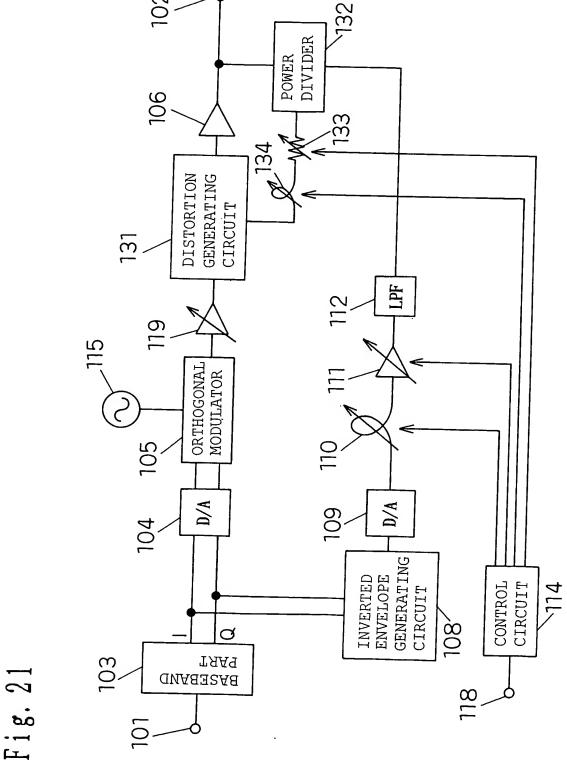
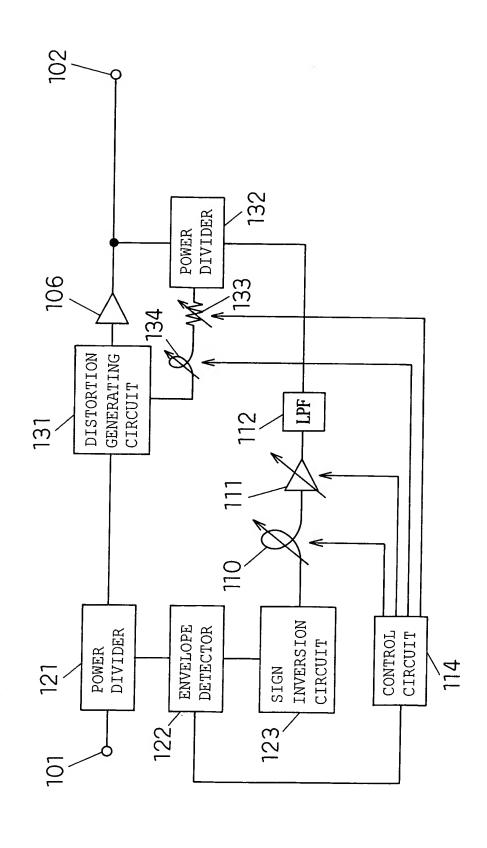


Fig. 19









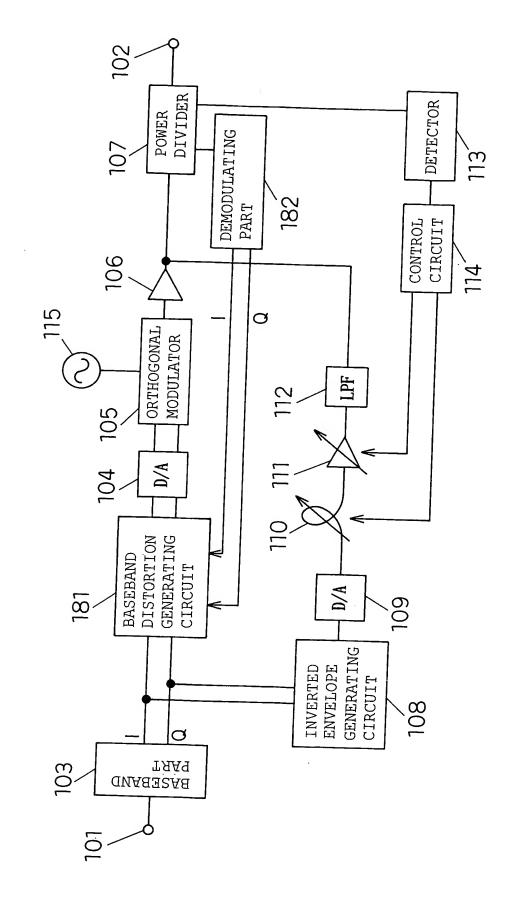
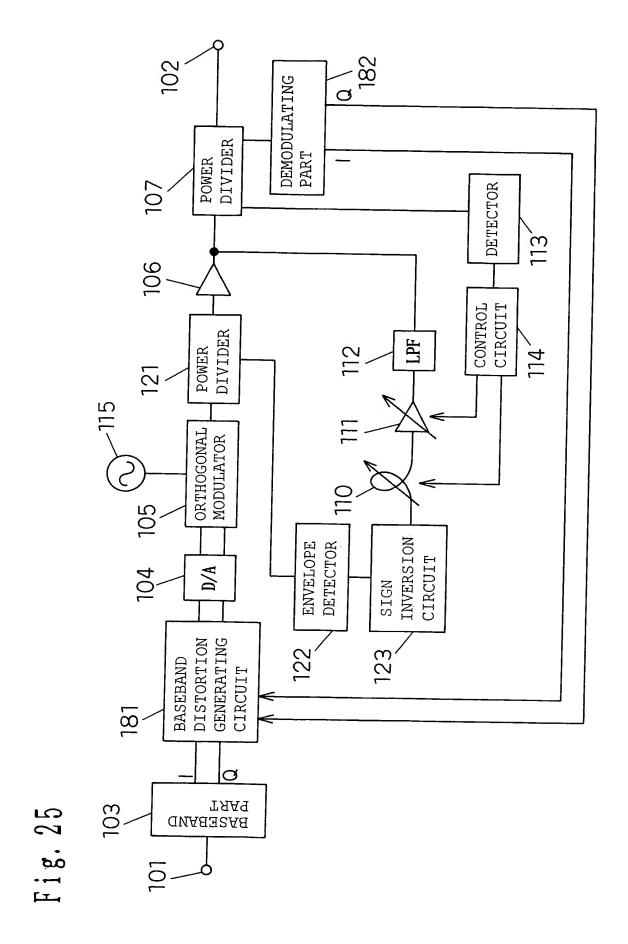


Fig. 23

DETECTOR POWER DIVIDER 113 DEMODULATING PART CIRCUIT 182 114 106 Q 105 ORTHOGONAL MODULATOR LPF D/A DISTORTION GENERATING BASEBAND CIRCUIT 18 BASEBAND PART بت



DEMODULATING PART 182 CONTROL CIRCUIT 106 Ø ORTHOGONAL MODULATOR LPF GENERATING CIRCUIT DISTORTION BASEBAND D/A 109 181 INVERTED ENVELOPE GENERATING CIRCUIT 108 Q BASEBAND PART Fig. 26

LPF

DETECTOR

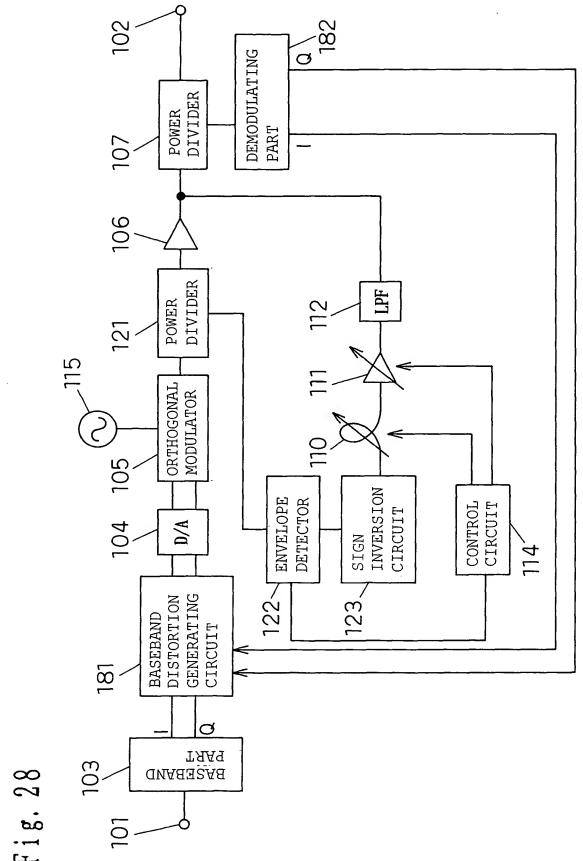
102

107

POWER DIVIDER

POWER DIVIDER DEMODULATING 106 107 182 PART 119 Ø ORTHOGONAL MODULATOR GENERATING DISTORTION BASEBAND CIRCUIT CONTROL CIRCUIT D/A 109 18 INVERTED ENVELOPE GENERATING CIRCUIT 108 118, BASEBAND TAAT

Fig. 27



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Fig. 29 PRIOR ART

